

OCT 31 2005

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 10/822,030  
Confirmation No. .... 1688  
Filing Date ..... April 8, 2004  
Inventor..... Nishant Sinha  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2818  
Examiner ..... Ngan V. Ngo  
Attorney's Docket No. .... MI22-2351  
Customer No. .... 021567  
Title: Semiconductor Processing Methods For Forming Electrical Contacts

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR § 1.8**

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at 571-273-8300 on the date shown below:

1. Certificate of Facsimile Transmission
2. Response to September 6, 2005 Office Action
3. Evidence of Assignment (Kirby)
4. Evidence of Assignment (Rigg)

Dated: October 31, 2005By: 

Jennifer Gaines, Legal Assistant  
Telephone No. (509) 624-4276  
Facsimile No. (509) 838-3424

**FEE DEFICIENCY**

Although it is believed that no fees are due, the Commissioner is hereby authorized to charge any fees under 37 C.F.R. 1.16 and 1.17 which may be required by this paper to Deposit Account No. 23-0925.

Dated: October 31, 2005By: 

David G. Latwesen, Ph.D.  
Reg. No. 38,533

NUMBER OF PAGES IN FACSIMILE: 20

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**RESPONSE TO SEPTEMBER 6, 2005 OFFICE ACTION**

To: Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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**AMENDMENTS**

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